

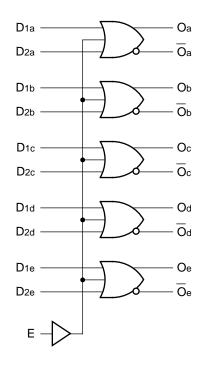
FEATURES

- Max. propagation delay of 700ps
- IEE min. of -45mA
- Industry standard 100K ECL levels
- Extended supply voltage option: VEE = -4.2V to -5.5V
- Voltage and temperature compensation for improved noise immunity
- Internal 75kΩ input pull-down resistors
- 50% faster than Fairchild 300K
- Function and pinout compatible with Fairchild F100K
- Available in 28-pin PLCC package

DESCRIPTION

The SY100S302 offers five 2-input OR/NOR gates designed for use in high-performance ECL systems. The five gates are controlled by a common Enable signal. All inputs have 75k Ω pull-down resistors and all outputs are buffered.

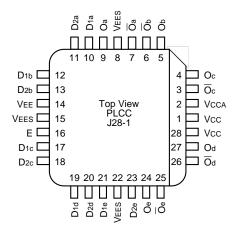
BLOCK DIAGRAM



PIN NAMES

Pin	Function
Dna – Dne	Data Inputs (n-15)
E	Enable Input
Oa – Oe	Data Outputs
Oa – Oe	Complementary Data Outputs
VEES	VEE Substrate
VCCA	Vcco for ECL Outputs

PACKAGE/ORDERING INFORMATION



Ordering Information

Part Number	Package Type	Operating Range	Package Marking	Lead Finish
SY100S302JC	J28-1	Commercial	SY100S302JC	Sn-Pb
SY100S302JCTR ⁽¹⁾	J28-1	Commercial	SY100S302JC	Sn-Pb
SY100S302JZ ⁽²⁾	J28-1	Commercial	SY100S302JZ with Pb-Free bar-line indicator	Matte-Sn
SY100S302JZTR ^(1, 2)	J28-1	Commercial	SY100S302JZ with Pb-Free bar-line indicator	Matte-Sn

Notes:

1. Tape and Reel.

2. Pb-Free package is recommended for new designs.

28-Pin PLCC (J28-1)

TRUTH TABLE⁽¹⁾

D1X	D2X	E	ох	ŌX
L	L	L	L	Н
L	L	Н	Н	L
L	Н	L	Н	L
L	Н	Н	Н	L
Н	L	L	Н	L
Н	L	Н	Н	L
Н	Н	L	Н	L
Н	Н	Н	Н	L

Note:

1. H = High Voltage Level

L = Low Voltage Level

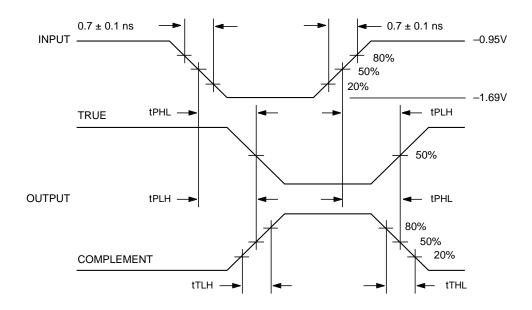
DC ELECTRICAL CHARACTERISTICS

VEE = $-4.2V$ to $-5.5V$ unless otherwise specified, VCC = VCCA = GND							
Symbol Parameter		Min.	Тур.	Max.	Unit	Condition	
Ін	Input HIGH Current, All Inputs	_	—	200	μΑ	VIN = VIH (Max.)	
IEE	Power Supply Current	-45	-28	-21	mA	Inputs Open	

AC ELECTRICAL CHARACTERISTICS

		TA :	TA = 0°C		TA = +25°C		TA = +85°C		
Symbol	Parameter	Min.	Max.	Min.	Max.	Min.	Max.	Unit	Condition
tplh tphl	Propagation Delay Data to Output	250	700	250	700	250	700	ps	
tPLH tPHL	Propogation Delay Enable to Output	250	900	250	900	250	900	ps	
tтlн tтнl	Transition Time 20% to 80%, 80% to 20%	300	900	300	900	300	900	ps	

TIMING DIAGRAM

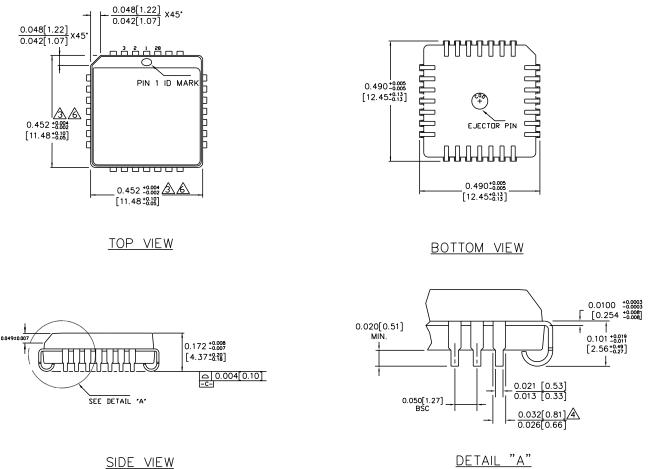


Propagation Delay and Transition Times

Note:

VEE = -4.2V to -5.5V unless otherwise specified, VCC = VCCA = GND

28-PIN PLCC (J28-1)



NOTES:

- DIMENSIONS ARE IN INCHES [MM]. 1.
- CONTROLLING DIMENSION: INCHES
- DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS, EITHER OF WHICH SHALL NOT EXCEED 0.008 [0.203]. LEAD DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. A
- 5.

MAXIMUM AND MINIMUM SPECIFICATIONS ARE INDICATED AS FOLLOWS: MAX/MIN PACKAGE TOP DIMENSION MAY BE SLIGHTLY ∕&∖ SMALLER THAN BOTTOM DIMENSION

DETAIL "A"

Rev. A

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